

**PATENT** 

In re application of:	
Barmore	) )
Application No: 09/607,680	Examiner: Casiano, A.
	Art Unit: 2182
Filed: June 30, 2000	I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient
For: MOTHERBOARD EXTENSION FEATURES TO PROVIDE PLUG AND PLAY	postage in an envelope addressed to Commissioner of Patents, Washington, D.C. 20231 on:
Information	2. 25.03
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Comprissioner For Patents	Name of Person Malling Correspondence
Washington, D.C. 20231	Rul S. W. C. 2.25.03

AMENDMENT AND RESPONSE

Dear Examiner:

In response to the Office Action mailed November 25, 2002, please enter this  $\mbox{RECEIVED}$ MAR 0 6 2003 amendment and consider the following remarks.

Signature

**Technology Center 2100** 

## IN THE CLAIMS

(Unchanged) A system comprising:

a motherboard to have a chipset coupled thereto;

a memory to store a sequence of instructions, the memory coupled with the

motherboard; and

a riser card coupled with the mother board, the riser card having a circuit thereon that interacts with a corresponding portion of the chipset to provide a functionality to the